# **REMARKS**

This Request for Continued Examination (RCE) Application is being filed in an abundance of caution to permit consideration of a Supplemental Information Disclosure Statement.

No new matter is being presented in this application.

A telephonic interview is requested in the event that the next office action is one other than a Notice of Allowance. The undersigned is available during normal business hours (Pacific Time Zone).

Respectfully submitted,

Dated: 08 Nov 2004

Bv:

James El. Lake Reg. No. 44,854

# NOV 0 8 2004 3

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application Serial No
ຳ Confirmation No
Filing Date June 11, 200
Inventor Vishnu K. Agarwal, et a
Assignee Micron Technology, Inc
Group Art Unit
Examiner C. Thompson
Attorney's Docket No
Customer No
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Title: Capacitor Forming Methods and Capacitor Constructions

## INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

## **REMARKS**

The citation listed may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §1.56. The Examiner is requested to make this citation of official record in this application. No admission is made regarding whether the submitted reference is prior art.

Respectfully submitted,

Dated: 08 Nov 2004

torney:

James **Ę**. Lake ̇̀

Reg. No.: 44,854

Form PTO-144			U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-1568			SERIAL NO. 09/879,335		
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